

Job Title:	PSU Senior Design Engineer/ PSU Design Engineer	Level/Salary Range:	PSU Design Engineer /Senior Engineer
Department	Development	Position Type:	Full time
		Travel Required:	No

Head line

An (MSc /PhD) or graduate engineer with min 3 years' experience of PSU design.

This is an exciting opportunity for a hands-on PSU Design Engineer to contribute and broaden experience:

- Specify and design future bench top and system PSUs for the test and measurement market. These have zero to full power voltage and current demands.
- Supervise and inspire junior engineers.
- Support existing product designs in production

Job Description

ROLE AND RESPONSIBILITIES

1. Develop products to agreed requirements, specification, cost and time scales.
2. Maintain product and company reputation by complying with required statutory regulations.
3. Specify manufacturing assembly processes, test methods, equipment and materials.
4. Evaluate applications, products and components by designing and conducting research programs.
5. Prepare product reports by collecting, analysing, and summarizing information and trends.
6. Provide engineering information and customer support by answering questions and requests.
7. Maintain professional and technical knowledge by attending educational workshops, reviewing professional publications, establishing personal networks and participating in professional societies.
8. Contribute to team effort by accomplishing related results as needed.

QUALIFICATIONS AND EDUCATION REQUIREMENTS

9. 3 years or more experience in AC-DC and DC-DC PSU design 100W to 1500W.
10. PhD, MSc or BSc in PSU or Electronics Design or related course.

REQUIRED SKILLS

11. Experience of SMPS design including specifying more than one of the following topologies: fly-back, resonant LLC, half bridge, full bridge, phase shift ZVS full bridge or buck converters.
12. Experience with low noise PCB layout and debug of PSU layouts to meet stringent noise and EMC requirements.
13. Proficient in designing and specifying magnetic components

PREFERRED SKILLS

14. Knowledge of analogue or DSP control loops
15. Knowledge of active or bridgeless PFC design.
16. Knowledge of international safety standards in particular IEC61010
17. An understanding of digital signal processing techniques, including digital filters, IQ sample manipulation etc.
18. Experience in test automation is a plus

ADDITIONAL NOTES: